

## Drafts

## BRS:

## Pending

## Active

- L1: (10184) (addition or add or adding or sum or summation) near7 (modular or modulo)
- L2: (3170) reset adj state same set state
- L3: (22) energy same resistanc\$2 same 2
- L4: (4) 1 and 3
- L5: (51) 1 and 2 not 4
- L6: (148) energy same resistanc\$2 and 1
- L7: (148) 6 not 5
- L8: (7374) intermediate adj state\$1
- L9: (4) 7 and 8
- L10: (176) phase adj change and 1
- L11: (4743199) (addition or add or adding or sum or summation)
- L12: (20) 3 and 11
- L13: (2) ("6448576").PN.

## Failed

## Saved

## Favorites

## Tagged (2)

## UDC

## Queue

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DBs US-PGPUB: USPAT: USOCR: EPO: JPO: DERWENT: IBI

Default operator: OR

☒ Highlight all hit terms initially

BRS form S&amp;R form Image Text HTML

	U	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval CI	Inventor	S	C	P
1	<input type="checkbox"/>	US 20040140523	20040722	13	Programmable resistance memory element with indirect heating	257/528	257/E23.149		Hudgens, Steve et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
2	<input type="checkbox"/>	US 6448576 B1	20020910	8	Programmable chalcogenide fuse within a semiconductor device	257/50	257/529; 257/E23.149;		Davis, John D. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

Hits Details HTML

Ready

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